

ABSTRACT OF THE DISCLOSURE

A non-inverting-side first switch is formed in a first area on one of sides of a differential amplifier, meanwhile a non-inverting-side second switch is formed in a second area on the other of sides of the differential amplifier, the non-inverting switches being for resetting a non-inverting input terminal of the differential amplifier. Similarly, An inverting-side first switch is formed in the first area, meanwhile an inverting-side second switch is formed in the second area, the inverting switches being for resetting a non-inverting input terminal of the differential amplifier. Further, a non-inverting-side line connecting the non-inverting-side first switch and the non-inverting input terminal, and an inverting-side line connecting the inverting-side first switch and the inverting-side second switch are provided next to each other. A signal line crossing one of the lines is provided so as to cross the other of the lines. This causes interferences from the signal line onto the lines to cancel out each other. As a result, it is possible to realize a switched capacitor circuit capable of attaining improvement in operational accuracy and low electric power consumption or high-speed operation.